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- OR gates using minimum number of NAND gates. 3. Give the truth table for EX-NOR and realize using NAND gates? 4. What are the logic low and High levels of TTL IC's and CMOS IC's? 5. Compare TTL logic family with CMOS family? 6.

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subtractor by using basic gates and universal gates with necessary expressions. 2. Design fulladder& full subtractor by using universal gates and using two half sub tractors basic half adders with necessary Boolean functions. 3.

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It has two inputs A and B and two outputs S (sum) and C (carry). It is represented by XOR logic gate and an AND logic gate. Truth Table of Half adder: 25) What is Full-Adder? Full-adder is the circuits that perform the addition of three bits. It has three inputs A, B and a carry bit. Full adders are represented with AND, OR and XOR logic gate.

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Laboratory Experiments: 1. Verify (a) Demorgan's Theorem for 2 variables. (b) The sum-of product and product-of-sum expressions using universal gates. 2. Design and implement (a) Full Adder using basic logic gates. (b) Full subtractor using basic logic gates. 3. Design and implement 4-bit Parallel Adder/subtractor using IC 7483. 4.

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## **DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING ...**

code for all logic gates, flip-flops, counters and adders etc. Students will be able to compile, simulate and synthesize the verilog code. From this lab the students will be able to draw the schematic diagram and layout for the inverter and amplifiers and verify their functionality.

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Question: Digital Logic Design Laboratory 264 Assignment 1 Summer 2019/2020 Design And Simulate A Combinational Circuit Using Quartus II Simulator. Your Design Must Read 4-bit BCD And Convert It To 7536 Number System. The Input Of The Circuit Will Be X3 X2 X1 XO And The Output F3 12 11 Fo.

### **Digital Logic Design Laboratory 264 Assignment 1 S ...**

design combinational logic circuits • Combinational logic circuits do not have an internal stored state, i.e., they have no memory. Consequently the output is solely a function of the current inputs. • Later, we will study circuits having a stored internal state, i.e., sequential logic circuits.

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